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Bib Data Sheet

CONFIRMATION NO. 3437

SERIAL NUMBER 09/843,687	FILING DATE 04/30/2001 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 43889-937
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** CONTINUING DATA ***** *Yes TO*

This application is a CON of 08/803,145 02/19/1997 PAT 6,282,506

** FOREIGN APPLICATIONS ***** *Yes TO*

JAPAN 8-032152 02/20/1996

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/15/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	JAPAN	26	<i>11 4</i>	10

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TITLE

Method of edsigning semiconductor integrated circuit

FILING FEE

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